Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	847871	(phase adj lock$3 or pll) and second adj$3 scaling adj circuit	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 14:05
S2	6	(phase adj lock$3 or pll) and second adj3 scaling adj circuit	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 14:09
S3	8	(phase adj lock$3 or pll) and second adj3 divider and internal adj circuit$2 and external adj circuit$2	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 15:16
S4	1737	(phase adj lock$3 or pll) and second adj3 divider	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 14:14
S5	15	(phase adj lock$3 or pll) and second adj3 divider and timing with internal with circuit$2 and timing with external with circuit$2	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 15:15
S6	14	("5034967" | "5056120" | "5410263" | "5481731" | "5485602" | "5675274" | "5811998" | "5862373" | "5909563" | "5944834" | "5964880" | "6041093" | "6092210" | "6128025").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/03/14 15:07
S7	0	("6675249").URPN.	USPAT	OR	ON	2005/03/14 15:14
S8	854	(phase adj lock$3 or pll) and (second or multiple or plural$3) adj3 divider with (vco or oscillator)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:13
S9	59	(phase adj lock$3 or pll) and (second or multiple or plural$3) adj2 programmable adj3 divider with (vco or oscillator)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:12
S10	59	S9 not (S2 or S3 or S5 or S6)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 15:16
S11	4	("5140284" | "5180992" | "5184350" | "5278814").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/03/14 15:36
S12	8	("5703537").URPN.	USPAT	OR	ON	2005/03/14 15:45
S13	9	(phase adj lock$3 or pll) and (third or multiple or plural$3) adj2 programmable adj3 divider with (vco or oscillator)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 15:56

S14	27	(phase adj lock$3 or pll) with (second or third or multiple or plural$3) adj2 programmable adj3 divider with output	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 15:56
S15	41	S9 not S14	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:04
S16	38	S15 not (S11 or S12 or S13)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:14
S17	893	(phase adj lock$3 or pll) and (second or third or multiple or plural$3) adj3 divider with (vco or oscillator)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:13
S18	352	(phase adj lock$3 or pll) with (second or third or multiple or plural$3) adj3 divider with (vco or oscillator)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:13
S19	362	(phase adj lock$3 or pll) with (second or third or multiple or plural$3) adj3 divider with output	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:18
S20	221	S18 and S19	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:14
S21	196	S20 not (S11 or S12 or S13 or S9 or S14 or S2 or S3 or S5 or S6)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:19
S22	53	(phase adj lock$3 or pll) with (multiple or plural$3) adj3 divider with output	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:18
S23	49	S22 not (S11 or S12 or S13 or S9 or S14 or S2 or S3 or S5 or S6)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/14 16:19